

Applicant(s): Theeuwes et al.
Serial No. 10/569,171
Filed: February 22, 2006

Group Art Unit: 2815
Confirmation No. 7877
Examiner: Ho, Anthony

For: ELECTRIC DEVICE COMPRISING AN LDMOS TRANSISTOR

15 RESPONSE TO RESTRICTION/ELECTION

In the Office Action dated January 3, 2008, the Examiner has required a
20 restriction to one of the following inventions:

- In response, Applicants elect without traverse Invention I for prosecution. Claims 1-4, 6, 7 and 10 encompass the elected Invention I.

Applicants respectfully suggest that the pending claims in the patent application are distinct over the prior art and that the application is in condition for allowance. Accordingly, a notice of allowance is earnestly solicited.

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Respectfully submitted,
Theeuwien et al.

Date: February 1, 2008

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